

## MSHM80N07

### N-Channel 80-V (D-S) MOSFET

#### Description

The device is using trench DMOS technology. This advanced technology has been especially tailored to minimize  $R_{DS(ON)}$ , provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

The device meets the RoHS and Green Product requirement, 100% EAS guaranteed with full function reliability approved.

#### Features

- $R_{DS(ON)} = 7m\Omega @ V_{GS} = 10V$
- Super Low Gate Charge
- Excellent  $dv/dt$  Capability
- 100% EAS Guaranteed
- Green Device Available

#### Typical Applications

- Networking
- Load Switch
- Synchronous Rectifier
- Quick Charger

**Package type** : PDFN 3.3X3.3

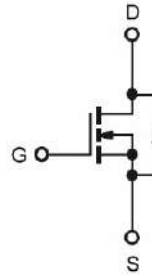
#### Packing & Order Information

3,000/Reel

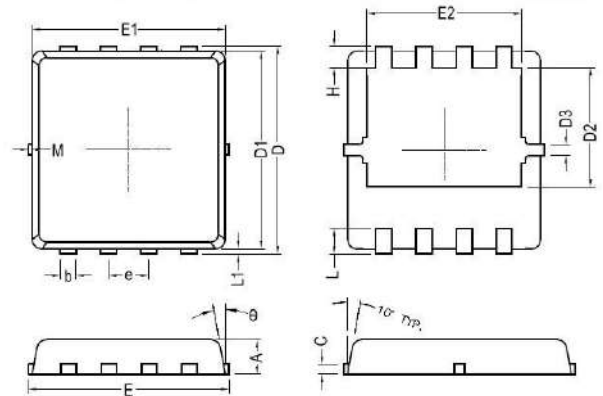


RoHS Compliant

#### Graphic Symbol

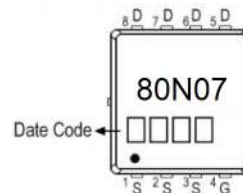


#### Package Dimension



REF.	Millimeter			REF.	Millimeter		
	Min.	Nom.	Max.		Min.	Nom.	Max.
A	0.70	0.75	0.80	E1	3.00	3.15	3.20
b	0.25	0.30	0.35	E2	2.39	2.49	2.59
C	0.10	0.15	0.25	e	0.65 BSC		
D	3.25	3.35	3.45	H	0.30	0.39	0.50
D1	3.00	3.10	3.20	L	0.30	0.40	0.50
D2	1.78	1.88	1.98	L1	-	0.13	0.20
D3	-	0.13	-	$\theta$	-	10°	12°
E	3.20	3.30	3.40	M	-	-	0.15

#### Marking



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#### MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

##### Absolute Maximum Ratings

Symbol	Parameter	Value	Units
$V_{DS}$	Drain-Source Voltage	80	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Continuous Drain Current <sup>1</sup> ( $T_C = 25^\circ\text{C}$ )	60	A
	Continuous Drain Current <sup>1</sup> ( $T_C = 100^\circ\text{C}$ )	38	A
$I_{DM}$	Pulsed Drain Current <sup>1,2</sup>	240	A
$I_{AS}$	Single Pulse Avalanche Current, $L = 0.1\text{mH}^3$	50	A
$E_{AS}$	Single Pulse Avalanche Energy, $L = 0.1\text{mH}^3$	125	mJ
$P_D$	Power Dissipation <sup>4</sup> ( $T_C = 25^\circ\text{C}$ )	60	W
$T_J/T_{STG}$	Operating Junction and Storage Temperature	-55 to 150	$^\circ\text{C}$

##### Thermal Resistance Ratings

Symbol	Parameter	Maximum	Units
$R_{\theta JA}$	Maximum Junction-to-Ambient <sup>1</sup>	62	$^\circ\text{C/W}$
$R_{\theta JC}$	Maximum Junction-to-Case <sup>1</sup>	2.05	$^\circ\text{C/W}$

##### Electrical Characteristics ( $T_J = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 250\mu\text{A}$	1.2	1.6	2.5	V
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS} = 0\text{V}$ , $I_D = 250\mu\text{A}$	80	-	-	V
$I_{GSS}$	Gate-Source Leakage Current	$V_{DS} = 0\text{V}$ , $V_{GS} = \pm 20\text{V}$	-	-	$\pm 100$	nA
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS} = 80\text{V}$ , $V_{GS} = 0\text{V}$ , $T_J = 25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{DS} = 64\text{V}$ , $V_{GS} = 0\text{V}$ , $T_J = 55^\circ\text{C}$	-	-	10	$\mu\text{A}$
$R_{DS(on)}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{GS} = 10\text{V}$ , $I_D = 8\text{A}$	-	5.8	7	$\text{m}\Omega$
		$V_{GS} = 4.5\text{V}$ , $I_D = 6\text{A}$	-	8.6	11	$\text{m}\Omega$
$g_{fs}$	Forward Transconductance	$V_{DS} = 10\text{V}$ , $I_D = 3\text{A}$	-	9	-	S
$E_{AS}$	Single Pulse Avalanche Energy <sup>5</sup>	$V_{DD} = 25\text{V}$ , $L = 0.1\text{mH}$ , $I_{AS} = 30\text{A}$	45	-	-	mJ
$V_{SD}$	Diode Forward Voltage <sup>2</sup>	$I_S = 1\text{A}$ , $V_{GS} = 0\text{V}$ , $T_J = 25^\circ\text{C}$	-	-	1.0	V
$I_S$	Continuous Source Current <sup>1,6</sup>	$V_G = V_D = 0\text{V}$ , Force Current	-	-	60	A
$I_{SM}$	Pulsed Source Current <sup>2,6</sup>		-	-	120	

##### Notes

1. The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 20Z copper.
2. The data tested by pulsed, pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
3. The  $E_{AS}$  data shows maximum rating. The test condition is  $V_{DD} = 50\text{V}$ ,  $V_{GS} = 10\text{V}$ ,  $L = 0.1\text{mH}$ ,  $I_{AS} = 50\text{A}$ .
4. The power dissipation is limited by  $150^\circ\text{C}$  junction temperature.
5. The Min. value is 100%  $E_{AS}$  tested guarantee.
6. The data is theoretically the same as  $I_D$  and  $I_{DM}$ , in real applications, should be limited by total power dissipation.

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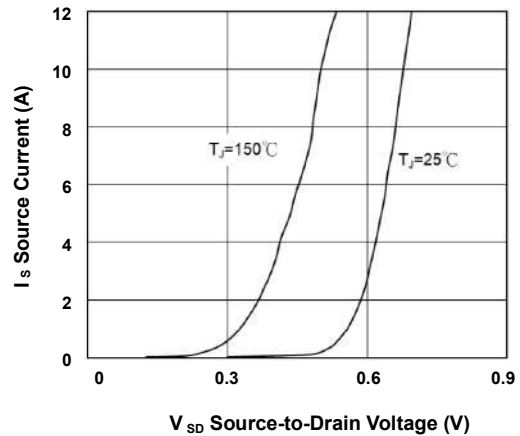
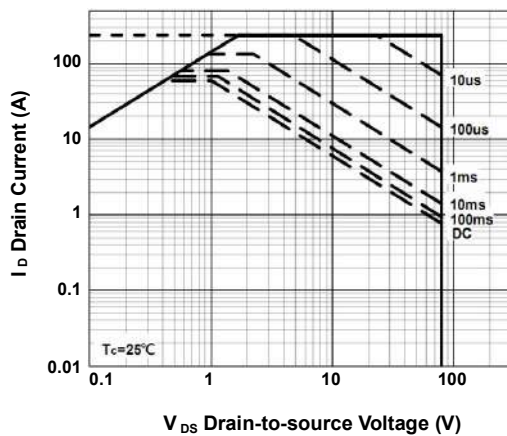
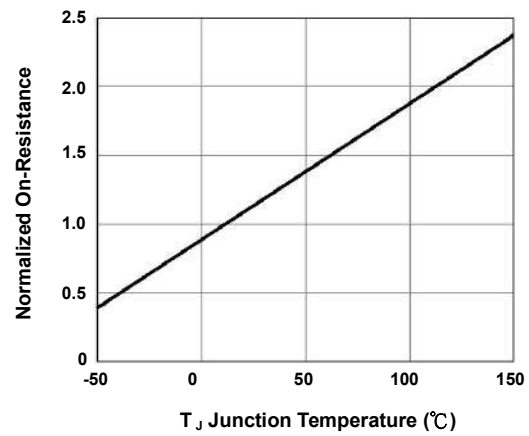
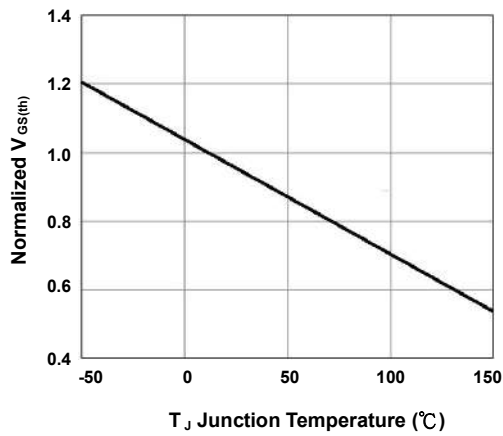
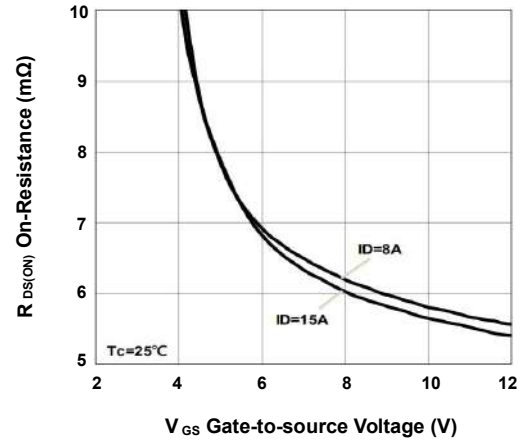
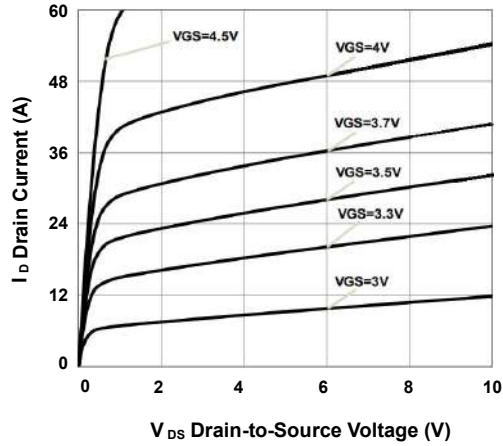
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Dynamic						
Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$Q_g$	Total Gate Charge <sup>2</sup>	$V_{DS} = 40V$	--	31	--	nC
$Q_{gs}$	Gate-Source Charge	$I_D = 30A$	--	4	--	
$Q_{gd}$	Gate-Drain Charge	$V_{GS} = 10V$	--	9.5	--	
$t_{d(on)}$	Turn-On Delay Time <sup>2</sup>	$V_{DS} = 40V$	--	22	--	ns
$t_r$	Rise Time	$I_D = 30A$	--	16	--	
$t_{d(off)}$	Turn-Off Delay Time	$V_{GS} = 10V$	--	40	--	
$t_f$	Fall Time	$R_G = 6\Omega$	--	31	--	
$C_{iss}$	Input Capacitance	$V_{DS} = 40V$	--	1720	--	pF
$C_{oss}$	Output Capacitance	$V_{GS} = 0V$	--	350	--	
$C_{rss}$	Reverse Transfer Capacitance	$f = 1.0MHz$	--	11	--	
$t_{rr}$	Reverse Recovery Time	$I_F = 10A, di/dt = 100A/\mu s, T_j = 25^\circ C$	--	40	--	nS
$Q_{rr}$	Reverse Recovery Charge		--	55	--	nC
$R_g$	Gate Resistance	$V_{DS} = 0V, V_{GS} = 0V, f = 1.0MHz$	--	1.1	--	$\Omega$

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### N-Channel 80-V (D-S) MOSFET

- Typical Electrical Characteristics



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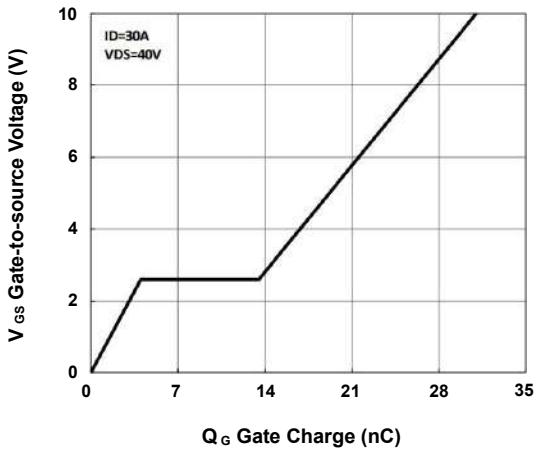


FIG.7-Gate Charge Characteristics

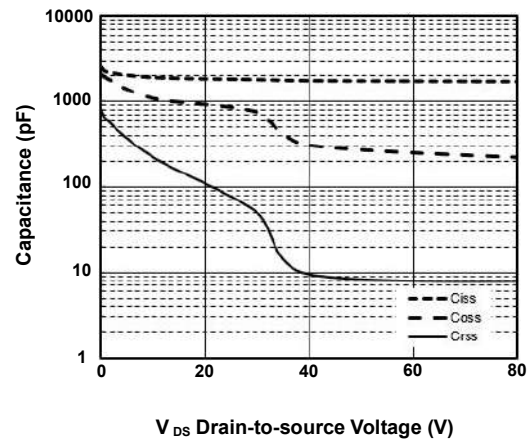


FIG.8-Capacitance Characteristics

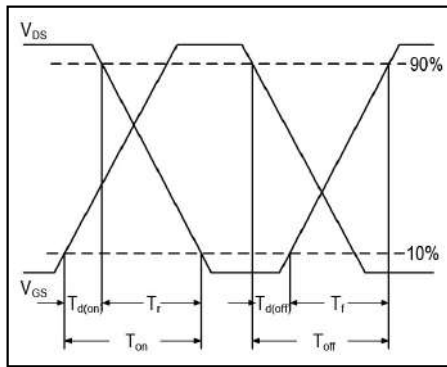


FIG.9-Switching Time Waveform

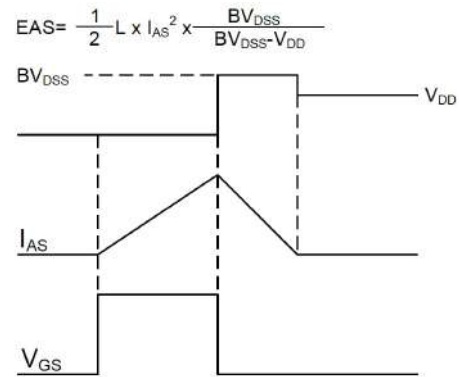


FIG.10-Unclamped Inductive Switching Waveform

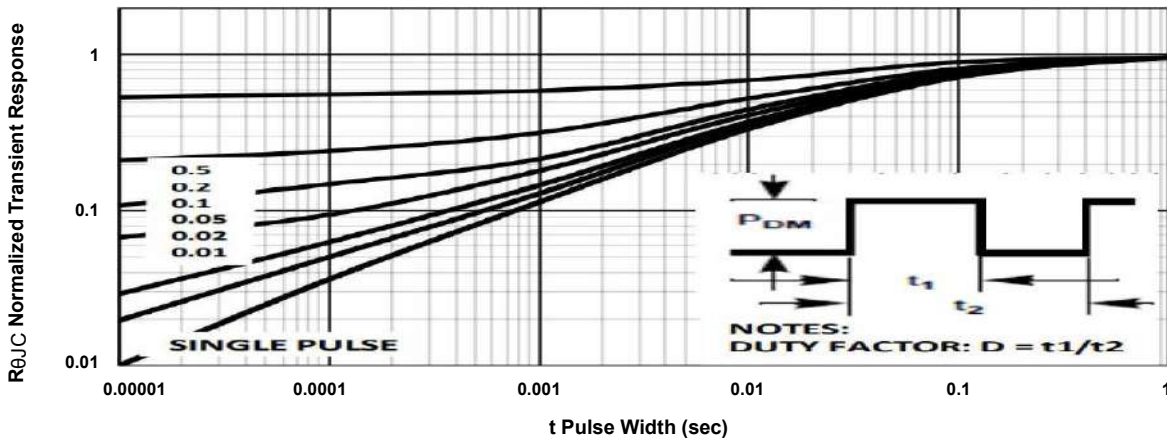


FIG.11-Normalized Maximum Transient Thermal Impedance

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